

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hyun-Chul Kim
Serial No.: 10/663,967
Filed: September 16, 2003

Confirmation No.: 2684
Group Art Unit: 2822
Examiner: Maria F. Guerrero

For: **METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES HAVING
FUSE STRUCTURES INCLUDING BUFFER LAYERS**

Date: December 6, 2006

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER ALLOWANCE

Sir:

Applicant provides the present Amendment After Allowance to address issues raised in the Notice of Allowance mailed November 21, 2006.

It is not believed that an extension of time and/or additional fee(s), including fees for additional claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

Remarks begin on page 2 of this paper.